

Improving Scalability of Sparse Direct Linear Solvers

X. Sherry Li

Lawrence Berkeley National Laboratory

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- Collaborators:
	- **James Demmel, UC Berkeley**
	- $\mathcal{L}_{\mathcal{A}}$ **Laura Grigori, INRIA, France**
	- –**Ming Gu, UC Berkeley**
	- $\mathcal{L}_{\mathcal{A}}$, and the set of th **Panayot Vassilevski, Lawrence Livermore National Lab**
	- **Jianlin Xia, UCLA**

Sparse direct linear solver

- Solve A x = b
	- **Example: A of dimension 10 6, only 10 ~ 100 nonzeros per row**
	- $\mathcal{L}_{\mathcal{A}}$ **No restriction on sparsity pattern (as opposed to structured matrices)**
- Algorithm: LU factorization: A = LU, followed by lower/upper triangular solutions
	- $\mathcal{L}_{\mathcal{A}}$, and the set of th **Store only nonzeros and perform operations only on nonzeros**
- Distinctions from dense solvers
	- **Need to accommodate fill-in elements**
	- – **Reorderings to maintain numerical stability, preserve sparsity, and maximize parallelism: Pr A P cT = L U**
	- **Irregular, indirect memory access; High communication-tocomputation ratio (latency-bound)**

Available codes

• Survey of different types of factorization codes

http://crd.lbl.gov/~xiaoye/SuperLU/SparseDirectSurvey.pdf

- **LLT (s.p.d.), LDLT (symmetric indefinite), LU (nonsymmetric), QR (least squares)**
- $\mathcal{L}_{\mathcal{A}}$ **Sequential, shared-memory, distributed-memory, out-ofcore**
- Distributed-memory solvers: usually MPI-based
	- **SuperLU_DIST [Li, Demmel, Grigori]**
		- **Accessible from PETSc, Trilinos**
	- **MUMPS, PasTiX, WSMP, . . .**

SuperLU software status

- With Fortran interface
- SuperLU_MT similar to SuperLU both numerically and in usage

SuperLU_DIST major steps: (parallelization perspectives)

- Static numerical pivoting: improve diagonal dominance
	- **Currently use MC64 (HSL); Parallelization underway [J. Riedy]**
- Sparsity-preserving ordering
	- **Can use ParMeTis**
- Symbolic factorization: determine pattern of {L\U}
	- $\mathcal{L}_{\mathcal{A}}$ **Being parallelized**
- Numerics: factorization, triangular solves, iterative refinement (usually dominate total time)
	- $\mathcal{L}_{\mathcal{A}}$, and the set of th **Parallelized a while ago; Need to improve load balance, latency-hiding**

Supernode

 \bullet Exploit dense submatrices in the L & U factors

- Why are they good?
	- **Permit use of Level 3 BLAS**
	- $\mathcal{L}_{\mathcal{A}}$ **Reduce inefficient indirect addressing (scatter/gather)**
	- – **Reduce symbolic factorization time by traversing a coarser graph**

Distribute the matrices

- Matrices involved:
	- **A, B (turned into X) – input, users manipulate them**
	- $\mathcal{L}_{\mathcal{A}}$ **L, U – output, users do not need to see them**
- A (sparse) and B (dense) are distributed by block rows

 Natural for users, and consistent with other popular packages: e.g. PETSc

2D block cyclic layout for {L\U}

- Good for scalability, load balance
- "Re-distribution" phase to distribute the initial values of A to the 2D block-cyclic data structure of L & U

nzval

- **All-to-all communication, entirely parallel**
- **< 10% of total time for most matrices**

• Sparsity-preserving ordering: MeTis applied to structure of A'+A

Performance on IBM Power5 (1.9 GHz)

• Up to 454 Gflops factorization rate

Performance on IBM Power3 (375 MHz)

•Quantum mechanics, complex mund

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Parallelizing symbolic factorization

- Serial algorithm is fast (usually < 10% total time) but requires entire structure of A, limiting memory scalability
- Parallel approach
	- $\mathcal{L}_{\mathcal{A}}$ **Use graph partitioning to reorder/partition matrix.**
		- •**ParMetis on structure of A + A'**
	- $\mathcal{L}_{\mathcal{A}}$, and the set of th **Exploit parallelism given by this partition (coarse level) and by a block cyclic distribution (fine level)**
- Summary of results
	- **Memory: up to 25x reduction of symbolic fact.**

up to 5x reduction of the entire solver

Runtime: up to 14x speedup of symbolic fact.

up to 20% faster of the entire solver

Matrix partition

- \bullet Separator tree
	- **Balanced tree with balanced data distribution**
	- $\mathcal{L}_{\mathcal{A}}$ **Exhibits computational dependencies**
		- **If node j updates node k, then j belongs to subtree rooted at k.**

Fluid flow (1/1)

- \bullet bbmat: $n = 38,744$, $nnz = 1.8$ M, 34 M fill-ins using ParMetis on one processor
- \bullet Memory usage:
	- **SFseq (symbolic sequential), SFpar (symbolic parallel)**
	- $\mathcal{L}_{\mathcal{A}}$, and the set of th **Entire solvers: SLU_SFseq, SLU_SFpar**

Fluid flow (2/2)

• Runtime in seconds

Fast solver

- •In the spirit of fast multipole, but for matrix inversion
- \bullet Model problem: discretized system $Ax = b$ from certain PDEs, e.g., 5-point stencil on $k \times k$ grid, $n = k^2$
- \bullet Nested dissection ordering gave optimal complexity in exact arithmetic [Hoffman/Martin/Ross]
	- –**Factorization cost: O(n1.5) (3D: O(n2))**

Exploit low-rank property

- Consider top-level dissection:
- S is full
	- **Needs O(k 3) to find u 3**

$$
\begin{pmatrix} A_{11} & 0 & A_{13} \ 0 & A_{22} & A_{23} \ A_{31} & A_{32} & A_{33} \end{pmatrix} \begin{pmatrix} u_1 \ u_2 \ u_3 \end{pmatrix} = \begin{pmatrix} f_1 \ f_2 \ f_3 \end{pmatrix}
$$

$$
S u_3 = f_3 - A_{31} A_{11}^{-1} f_1 - A_{32} A_{22}^{-1} f_2
$$

• But, off-diagonal blocks of S has low numerical ranks (e.g. 10~15)

– **u 3 can be computed in O(k) flops**

- Generalize to multilevel dissection: all diagonal blocks corresp. to the separators have the similar low rank structure
- Low rank structures can be represented by hierarchical semiseparable (HSS) matrices [Gu et al.] (… think about SVD)
- Factorization complexity … essentially linear
	- **2D: O(p k 2), p is related to the problem and tolerance (i.e., numerical rank)**
	- **3D: O(c(p) k 3), c(p) is a polynomial of p**

Results of the model problem

 \bullet Flops and runtime comparison

Summary

- Current factorization algorithms can scale to 1000s processors
- New "fast solver" has potential of scaling to tera/petascale; demonstration remains open